N£ Page 18, line 18, delete "cost" and substitute --costs--, delete "draw" and substitute --draws--.

Page 35, line 16, after "Figure", delete "21" and substitute --22--.

Page 59, line 35, delete "used" and substitute --use--.

IN THE CLAIMS

Cancel claims 58-62.

Add the following new claims:

B1 Sub. El...

--71. A microprocessor system comprising:

a central processing unit;

memory;

a bus connecting said central processing unit to said memory;

instruction fetching means that are connected to said bus to fetch instructions for said central processing unit on said bus from said memory;

an instruction register for receiving fixed-length instruction groups that consist of multiple sequential instructions from said bus from said instruction fetching means;

instruction supplying means that are connected to said instruction register for supplying said multiple sequential instructions in succession from said instruction register;

a counter that is connected to control said instruction supplying means in order to supply the instructions in succession;

instruction decoding means that are connected to receive the instructions in succession from the instruction supplying means; and

said counter being connected to said instruction decoding means to receive counter control signals from said instruction decoding means.

72. The microprocessor system of claim 71 further comprising: instruction decoding means that are configured to supply a reset using said counter control signals to said counter and to supply control signals to said instruction fetching means such that the next said fixed-length instruction group is supplied to said instruction register in response to a SKIP instruction in said multiple sequential instructions.

2.